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| Notice of References Cited | Application/Control No. 09/666,277 | Applicant(s)/Patent Under Reexamination BOERSTLER, DAVID WILLIAM | |
| | Examiner Sudhanshu C. Pathak | Art Unit 2634 | Page 1 of 1 |

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| | U | Bill Bowden; http://ourworld.compuserve.com/homepages/Bill_Bowden/page9.htm ; Pages 1-9 ("Monostable flip-flop" on page 6). |
| | V | Digit Circuit; www.silicom.com/~pelican2/DIGIT_CIRCUIT.html ; Pages 1-4 ("Anti-Jitter circuit" on page 4). |
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.